



Organizer:



Essential Principles of Signal Integrity (EPSI)

April 26-27th, 2011

&

Differential Pair Design (DPD) April 28th, 2011

Intensive Workshops by Dr. Eric Bogatin of Bogatin Enterprises and beTheSignal.com

We are bringing the expert to your doorstep!

Polar Instruments (Asia Pacific) Pte Ltd has partnered with Dr. Eric Bogatin, world's leading trainer and Signal Integrity Evangelist for High Speed PCB Design, Signal Integrity, Testing, Characterization and verification and to bring his expertise to Asia for the benefit of the PCB Design and Manufacturing industry in the region.

Now with hands on labs!

New in 2011 classes, we show you how to use a very easy to use simulation tool, QUCS, and how you can quickly answer common signal integrity problems. This tool runs on any laptop with a Windows OS. We provide you a copy of the tool and all the circuits used in the labs. These are yours to take back.

No previous experience is necessary. Even if you have never done any simulation before, you will find this an incredibly easy tool to use. If you are familiar with SPICE, you will find QUCS to be far easier and more versatile.

To participate in the hands on labs, you must bring your own laptop to the class.

Ideal for Design and Fab Engineers!
REGISTER NOW!

polarinstruments.asia

Email or Fax the completed form to
training@polarinstruments.asia or +65 6873 7471

Or call +65 6873 7470

OR

mims@shrdc.org.my

Tel: 03-5513 3560

Fax: 03-5513 3490

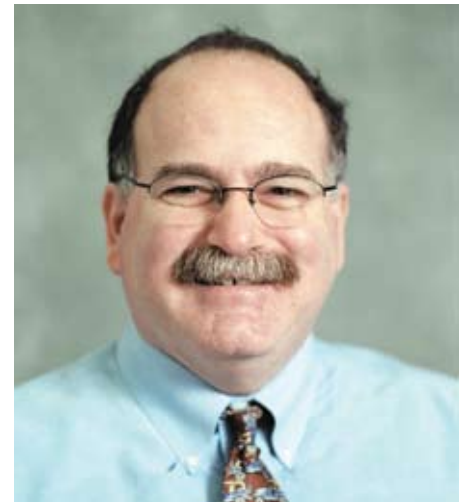
Supporting Partner:



April 26-28th, 2011

9.00am - 5.00pm

Eastin Hotel, Penang, Malaysia



Dr. Eric Bogatin - President, Bogatin Enterprises, LLC

Dr. Bogatin received his BS degree in physics from MIT, and MS and PhD degrees in physics from the University of Arizona in Tucson. He has written five books on signal integrity and interconnects design, over 300 papers and articles and has taught over 4,000 engineers in the last 25 years. He is a distinguished lecturer for the IEEE EMC society and lectures worldwide on signal integrity topics.

DON'T MISS THESE VALUABLE CLASSES!

Registration Fee (EPSI):
RM 3750*

Special Early bird Fee:
RM 3350*

(before 31st March 2011)

PLUS: 5% -10 % group discounts
Double deal** : 10% off for registering for both classes

*Price includes course materials, lunch and 2 tea breaks.

**Also sign up for Differential pair Design at the venue on April 28th.

Registration Fee (DPD) : RM 1600

Early Bird: RM 1450 (before 31st Mar 2011)

SHRDC Members Discount - EPSI : RM 200 / DPD : RM 100

EPSI: Essential Principles of Signal Integrity

Build your engineering intuition

The three critical steps to eliminate signal integrity problems from your product are identify the problem, find the root cause and turn the root cause into a design guideline. But every product is unique and custom. Designing a robust and cost effective product is not about blindly following a general set of design rules, rather it is about following a process that helps you apply your engineering intuition to balance cost and design tradeoffs specific to your product.

Learn this process used by thousands of engineers and build your engineering intuition with this two-day class, designed and offered by Signal Integrity Evangelist Dr. Eric Bogatin. In this introductory class the math is stripped away to reveal the underlying truth of how interconnects affect signal integrity. The most essential principles of signal integrity are introduced and reviewed, including principles of:

• Characteristic impedance, return current	• Ground bounce and cross talk
• Reflections	• Bandwidth
• Inductance	• Terminations
• Impedance	• PDN and EMI

Each of these principles is illustrated by examples of measurements or simulations using structures such as IC packages, connectors, printed circuit boards and cables.

Class Outline

Day One

Signal Integrity Problems and Solutions

- The three most important principles in signal integrity
- The six families of signal integrity problems and their solutions
- The ten habits of highly successful designers

Characteristic Impedance and Return Currents

- The instantaneous impedance all signals see
- Characteristic impedance and transmission lines
- Return currents in transmission lines

Reflections, Terminations and Topologies

- The origin of reflections
- Measuring and simulating reflections
- Termination and topology strategies

Hands on Lab

- The TDR and transmission lines
- Driving a transmission line
- Termination, topology and power consumption
- Signal quality with a discontinuity
- How long a stub is too long

Day Two

Cross Talk in Transmission Lines

- Capacitive and inductive coupling
- Influence of propagation direction on cross talk
- The origin of NEXT and FEXT in coupled lines
- Minimizing NEXT and FEXT by design
- Creating design rules for acceptable cross talk

Ground Bounce

- The physical basis of inductance
- Total inductance and the return path
- Minimizing ground bounce in packages, connectors, vias and planes
- The importance of return vias

PDN and EMC design

- The target impedance and impedance profiles
- Selecting decoupling capacitor values
- Common currents as the source of emissions
- Reducing common currents by design
- Reducing emissions with band aides

Hands on Lab

- Simulating ground bounce
- Ground bounce and termination
- An impedance analyzer
- Impedance profile of 1, 3, and 10 capacitors
- Reducing peak impedances by design

Visit bethesignal.com or polarinstruments.asia for more details

DPD: Differential Pair Design

Overcoming the obstacles in high speed serial channels

The cure for FUD (Fear, Uncertainty and Doubt)

Are you designing one of the alphabet soup high speed serial links like PCIe, SATA, SAS, XAUI, GigE, USB or LVDS? Then all of your interconnects are differential pairs and eliminating signal integrity problems in your design will determine whether your product works or not.

This one-day, intensive training will bring you “up to speed” on how to design the physical interconnects of your channel to improve signal quality and achieve the bit rate you need. We eliminate the myth-conceptions that dominate the industry and show you the right way of designing differential pairs that operate above 10 Gbps.

We eliminate the confusion over:

• Tight or loose coupling	• Transparent via design
• Differential mode vs odd mode impedance	• Mode conversion and length matching
• Conductor loss and copper roughness	• S-parameters without tears
• Dielectric loss	• The limits to FR4

Outline

Module 1: Differential pairs

- The four multi gigabit problems
- Differential and common signals
- Differential impedance, odd mode impedance
- Stack up design: walk the line principle
- Channel to channel cross talk
- Tight or loose coupling?

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Module 2: Losses and S-parameters

- Why frequency dependent loss is important
- Losses and jitter
- Conductor and dielectric loss
- SDD21 and attenuation
- Using equalization to compensate for attenuation
- Length- bandwidth tradeoffs

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Module 3: Differential circuits

- Simulating differential pairs
- Unscrambling eyes: SBR, PDA, step response, PRBS
- Routing topologies and terminations
- Mode conversion and asymmetry
- When to terminate the common signal
- Impact from vias and discontinuities

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Module 4: Hands on Lab

- Differential and common signals: transient and single bit response
- Mode conversion and terminating common signals
- Impact from vias and stubs
- Impact from connectors
- Simulating the S-parameters: losses and discontinuities

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- April 28th 2011, Eastin Hotel Penang 9.00 a.m.- 5.00 p.m.
- Regular Price: RM 1600
- Early Bird Price: RM 1450 (before 31st March 2011)
- Save further 10% by signing up for Essential Principles on Signal Integrity (EPSI) course on April 26-27th 2011.

Visit bethesignal.com or polarinstruments.asia for more details

RECHARGE your Engineering Skills for SI Design, Test & Verification.

Also Available:

PUBLIC / IN-HOUSE CLASSES (2 Days) & BOOT CAMPS (1 Day)

Courses on High Speed Signal Integrity Design, Principals, Testing, Characterization and Validation techniques. Intensive one day boot camps on designing for high-speed serial links like PCIe, SATA, SAS, XAUI, GigE, USB or LVDS or designing, controlling, and characterizing transmission line losses.

ONLINE CERTIFICATION / CONTINUING EDUCATION COURSES / TRAINING - WEBINARS / LECTURES / RESOURCES

Online Certification courses on topics like essential Principals of Signal Integrity, Continuing Education Courses (CEC) and other training courses through “**No Myths Allowed**” webinars and lectures.

CORPORATE / INDIVIDUAL – ANNUAL or QUARTERLY SUBSCRIPTIONS

Time limited – annual, quarterly and soon to be offered monthly - Corporate / Individual subscriptions are available to access a wealth of resources related to SI design, test, characterization and validation.

Other Courses from Bogatin Enterprises LLC., USA :

PDN : Power Delivery Networks

Topics covered include:

- How do you select capacitors? How many, what value?
- Where should they be placed?
- When does location matter?
- How will you know if you got it right?
- What’s important in the stack up design?
- When is it worth it to use ultra thin laminates?
- What are good habits every layout designer should know?

SPSI : S parameters for SI

Topics covered include:

- The value of Insertion and return loss
- Single ended and Differential parameters
- How to extract characteristic impedance and differential impedance
- Identifying mode conversion problems and solutions
- The ten item check list to evaluate all S-parameters
- The four most important patterns you will see and what they tell you

TVD : Transparent Via Design

Topics covered include:

- Single ended Vias
- Differential Vias
- Corners, bends and serpentines
- Neck downs in BGA fields
- Solder balls
- Connectors
- Terminating resistors
- DC blocking capacitors

For more details visit: www.bethesignal.com and www.polarinstruments.asia

About Polar Instruments, Asia Pac

Headquartered in Singapore and with offices in Japan, Polar Instruments (Asia Pacific) Pte Ltd was established in 1999, as a wholly owned subsidiary of Polar Instruments Ltd, Guernsey, UK to provide sales, marketing and after sales support to customers in the then emerging Asia Pacific region. Now an independent company, Polar Asia Pac provides a range of value added services that include market research and development, product sales, marketing, and after sales support for our principals in the Asia Pacific region covering Australasia, Japan, to the Middle East. Our services include application support, repair & calibration, on-site/off-site maintenance services, and a range of professional consulting services for training, design, test, failure/data analysis and co-relation studies related to our current market space.

www.polarinstruments.asia

About Selangor Human Resource Development Centre (SHRDC)

SHRDC is public-private partnership set up with the objective of developing world class talent in Malaysia. Awarded the ISO 9001:2008, it is benchmarked as Malaysia’s premier center for competency based learning. It offers a wide range of high quality courses and certifications in Essential Skills, Electronics and Electrical, Information and Communications Technology, Microsystems and Plastics Technology., SHRDC is headquartered in Shah Alam where it also has a learning Centre with state-of-the-art facilities designed for provision of high quality training.

www.shrdc.org.my

Polar Instruments (Asia Pacific) Pte Ltd • T: +65 6873 7470 • F: +65 6873 7471 • E: training@polarinstruments.asia

Selangor Human Resource Development Centre • T +603 5513 3560 • F + 603 5513 3490 • E: info@shrdc.org.my

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