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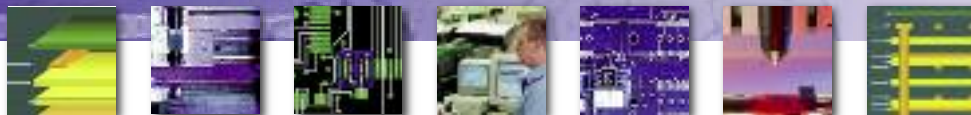
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Speedflex for Speedstack

Adds flex-rigid PCB layer stackup design and documentation to Speedstack

Speedflex allows PCB fabricators and OEM engineers to create and document flex-rigid PCB layer stackups using the Speedstack family of PCB design tools.



Speedflex allows OEM designers to create an accurate and efficient flex-rigid PCB stackup in minutes, with error-free documentation for tighter control over the finished board. For PCB fabricators, Speedflex provides the flexibility to quickly calculate the possible impact of substituting alternative materials, to improve manufacturability and reduce cost, whilst maintaining the specified parameters and performance of the board.

Easy graphical stackup display

The Speedflex navigator quickly guides engineers through the stackup process, and presents the complete assembly in a graphical display which highlights the flex regions within the flex-rigid PCB. User-definable settings, within the Speedflex navigator, allow engineers to display layers in transparent, invisible or 3D mode. A range of materials, including flexible adhesives, bondply and FlexiCore, can be enabled or disabled for each layer, and impedance structures can be added to each sub-stack.

Speedflex for cost control

Speedflex allows OEM designers and PCB fabricators to control the traditionally high cost of flex-rigid PCBs. Using comprehensive standard, or custom, libraries of suppliers, material leadtimes and costs, Speedflex can eliminate hours of complex calculations by quickly displaying the predicted effect of using different materials and suppliers on a board's electrical performance, cost and manufacturing yield. This ultra-fast process means that more options can be explored to find the optimum combination of materials and suppliers for each board.

Error-free documentation

Once the flex-rigid stackup is complete, Speedflex also adds flex-rigid report capability to the Speedstack report generator. Documents can be exported in a range of industry-standard formats, including JPEG, Gerber and PDF files, so that every company and individual within the fabrication process, from PCB fabricators to brokers, can have the same clear instructions and parameters.

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